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Docket: 0756-935

IN THE UNITED STATES PATENT A	AND TRADEMARK OFFICE			
In re PATENT application of Shunpei YAMAZAKI et al. Serial No. 08/160,909 Filed: December 3, 1993 For: METHOD OF FABRICATING SEMICONDUCTOR DEVICES AND APPARATUS FOR PROCESSING A SEMICONDUCTOR	) Art Unit: 1107  Examiner: M. Wilczewski,  Description of the second of			
Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231				
Sir:				
Transmitted herewith is an amendment in the above-identified application.				
[ ] Small entity status of this application under 37 C.F.R. 1.9 and 1.27 has been established by a verified statement previously submitted.				
[ ] A verified statement to establish small entity status under 37 C.F.R. 1.9 and 1.27 is enclosed.				
[X] No additional fee is required.				
The fee has been calculated as shown below:				
AFTER PAID FOR  AMENDMENT	XTRA .			

Indep* Minus	**	x38/76	
First Presentation of Multiple			
Dependent Claim		+ 120/240	
	TOTAL	<del></del>	
[ ] A check in the	he amount of \$	to cover the filing	

- [X] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.
- [X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16 and 1.17 (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

enclosed.

Gerald J. Ferguson, Jr.

Registration No. 23,016

Sixbey, Friedman, Leedom & Ferguson, P.C. 2010 Corporate Ridge, Suite 600 McLean, Virginia 22102 (703) 790-9110

CERTIFICATE OF MAILING 1995 Certify that this corresponding is based on the corresponding to the corresponding to

hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope didressed to: Commissioner of Patents and reademarks, Washington, D.C. 20231, op 130-45

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9/9/95 Docket: 0756-935

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re	PATENT application of	)	
Shun	pei YAMAZAKI et al.	)	
Seria	l No. 08/160,909	)	Art Unit: 1107
Filed	: December 3, 1993	)	Examiner: M. Wilczewski
For:	METHOD OF FABRICATING	)	
	SEMICONDUCTOR DEVICES	)	
	AND APPARATUS FOR	)	
	PROCESSING A	)	
	SEMICONDUCTOR	)	Date: January 30, 1995

## AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT AND INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents and Trademarks Washington, D.C. 20231

Sir:

In response to the Office Action of December 30, 1995, please amend the subject application as follows:

## IN THE CLAIMS:

Cancel claims 1-7 without prejudice to applicants' right to file a division